



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application. No:	10/090,481
Filed:	March 4, 2002
Inventor(s):	Brian D. Emberling and Ewa M. Kubalska
Title:	USING OBSERVABILITY LOGIC FOR REAL-TIME DEBUGGING OF ASICs

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Examiner:	Le, Don P.
Group/Art Unit:	2819
Atty. Dkt. No:	5181-90200

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Alexandria, VA 22313-1450, on the date indicated below.

Jeffrey C. Hood

                      
Signature

7/7/2004  
Date

### COMMENT ON NOTICE OF ALLOWABILITY

### Mail Stop Issue Fee

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In the Notice of Allowability mailed April 15, 2004, the allowed claims were listed as Claims 1, 2, 4-6, 9-13, 16 and 20-28; however, Applicant notes the allowed claims should be **Claims 1, 2, 4-6, 9-13, 16, 20 and 26-28**.

## REMARKS

**Applicant respectfully submits that the allowed claims should be Claims 1, 2, 4-6, 9-13, 16, 20 and 26-28.**

No fees are believed necessary; however, the Commissioner is authorized to charge any fees which may be required, or credit any overpayment, to Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C. Deposit Account No. 501505\5181-90200.

Respectfully submitted,

QC 2

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